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General Description

The MAX8814 intelligent, stand-alone constant-current, constant-voltage (CCCV), thermally regulated linear charger is designed for charging a single-cell lithiumion (Li+) battery. The MAX8814 integrates the currentsense circuit, MOSFET pass element, thermalregulation circuitry, and eliminates the reverse-blocking Schottky diode to create the simplest and smallest charging solution for handheld equipment.

The IC controls the charging sequence from the prequalification state through constant-current fast-charge and the final constant voltage charge. Proprietary thermal-regulation circuitry limits the die temperature during fast-charging or when the IC is exposed to high ambient temperatures, allowing maximum charging current without damaging the IC.

The MAX8814 achieves high flexibility by providing an adjustable fast-charge current through an external resistor. Other features include an active-low control input (\overline{EN}) and an active-low input power-source detection output (\overline{POK}). The IC also features a booting assistant circuit that distinguishes input sources and battery connection and provides an output signal (ABO) for system booting.

The MAX8814 accepts an input supply range from 4.25V to 28V, but disables charging if the input voltage exceeds +7V to protect against unqualified or faulty AC adapters. The IC operates over the extended temperature range (-40°C to +85°C) and is available in a compact 8-pin thermally enhanced TDFN 2mm x 2mm package (0.8mm max height).

Applications

Cellular and Cordless Phones Smartphones and PDAs MP3 Players Digital Still Cameras USB Appliances Charging Cradles and Docks Bluetooth[®] Equipment

_Features

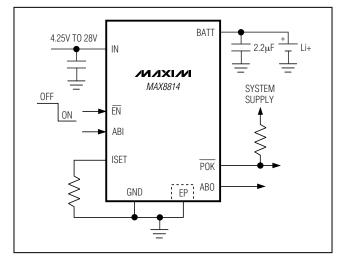
- CCCV, Thermally Regulated Linear 1-Cell Li+ Battery Charger
- No External MOSFET, Reverse Blocking Diode, or Current-Sense Resistor
- Programmable Fast-Charge Current (1ARMS max)
- Proprietary Die Temperature Regulation Control (+115°C)
- 4.25V to 28V Input Voltage Range with Input OVP Above +7V
- Charge-Current Monitor for Fuel Gauging (ISET)
- Low Dropout Voltage (300mV at 500mA)
- Input Power-Source Detection Output (POK), Charge-Enable Input (EN)
- Soft-Start Limits Inrush Current
- Output for Autobooting (ABO)
- Tiny 2mm x 2mm, 8-Pin TDFN Package, 0.8mm Height (max)

Ordering Information

PART	TEMP	PIN-	PKG	TOP
	RANGE	PACKAGE	CODE	MARK
MAX8814ETA+	-40°C to +85°C	8 TDFN 2mm x 2mm	T822+2	ABI

+Denotes a lead-free and RoHS-compliant package.

Typical Operating Circuit



Bluetooth is a registered trademark of Bluetooth SIG.

Pin Configuration appears at end of data sheet.

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For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, com or visit Maxim's website at www.maxim-ic.com.

MAX8814

ABSOLUTE MAXIMUM RATINGS

IN to GND	0.3V to +30V
ABI, BATT, EN, POK to GND	0.3V to +6V
ABO to GND	0.3V to (V _{BATT} + 0.3V)
ISET to GND	-0.3V to +4V
IN to BATT Continuous Current	1A _{RMS}
Continuous Power Dissipation (T _A =	+70°C)
8-Pin TDFN (derate 11.9mW/°C at	pove +70°C)
(multilayer PCB)	

BATT Short-Circuit Duration	Continuous
Operating Temperature Range	40°C to +85°C
Junction Temperature	+150°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering, 10s)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{IN} = 5V, V_{BATT} = 4V, R_{\overline{POK}} = 1M\Omega$ to BATT, $\overline{EN} = 0V, R_{ISET} = 2.8k\Omega$ to GND, $C_{BATT} = 2.2\mu$ F, $T_A = -40^{\circ}$ C to +85°C, unless otherwise noted. Typical values are at $T_A = +25^{\circ}$ C.) (Note 1)

PARAMETER	CON	IDITIONS	MIN	ТҮР	MAX	UNITS
Input Voltage Range			0		28	V
Input Operating Voltage Range			4.25		6.50	V
Power-OK Threshold	V _{IN} - V _{BATT} ,	V _{IN} rising		40	r	mV
Fower-OK Threshold	10mV hysteresis (typ)	V _{IN} falling		0 .25 40 30 3.5 0.8 0.23 0.021 0.021 1 3 179 4.200 158 2.2 24 570 39 107 4 10 +115		IIIV
Overvoltage-Lockout Trip Threshold	V _{IN} rising, 100mV hysteres	sis (typ)	6.5	7	7.5	V
	Constant-current charging	mode ($I_{BATT} = 0A$)		0.8	1.35	
IN Input Current	IC disabled ($V_{\overline{EN}} = 5V$)			0.23	0.50	mA
in input Current	$V_{IN} = 4V,$	$T_A = +25^{\circ}C$		0.021	0.065	ШA
	$V_{BATT} = 4.2V$	$T_A = +85^{\circ}C$		0.021		
BATT Input Current	$V_{IN} = 0$ to 4V, $V_{BATT} = 4.2V$			1	10	μA
	IC disabled			40 30 7 0.8 0.23 0.021 0.021 1 3 4.200 4.200 2.2 570 570 570 107 107 107 107 10 +115 2.5		
Battery Regulation Voltage	I _{BATT} = 0A	$T_A = 0^{\circ}C \text{ to } +85^{\circ}C$	4.179	4.200	4.221	V
Ballery negulation voltage	IBATT = 0A	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	4.158	4.200	4.242	v
Minimum BATT Bypass Capacitance				2.2		μF
)/ 2.5\/	$T_A = 0^{\circ}C \text{ to } +85^{\circ}C$	524	570	616	
Fast Charge Current	$V_{BATT} = 3.5V$	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	656		
Fast-Charge Current	$V_{BATT} = 3.5V,$	$T_A = 0^{\circ}C \text{ to } +85^{\circ}C$	89	0.8 0.23 0.021 0.021 1 3 4.179 4.200 4.158 4.200 2.2 524 570 485 570 89 107 80 107 4 10 +115	125	mA
	$R_{ISET} = 14k\Omega$	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	80		134	
Prequalification Charge Current	Percentage of the fast-char $T_A = 0^{\circ}C$ to $+85^{\circ}C$	rge current, V _{BATT} = 2.2V,	4	10	15	%
Die Temperature Regulation Threshold				+115		°C
V _{BATT} Prequalification Threshold Voltage	VBATT rising, 100mV hyste	resis (typ)	2.3	2.5	2.7	V
Current-Sense Amplifier Gain (IBATT to I _{ISET})	IBATT = 570mA			877.2		µA/A

M/XI/M

www.daELECTRICAL CHARACTERISTICS (continued)

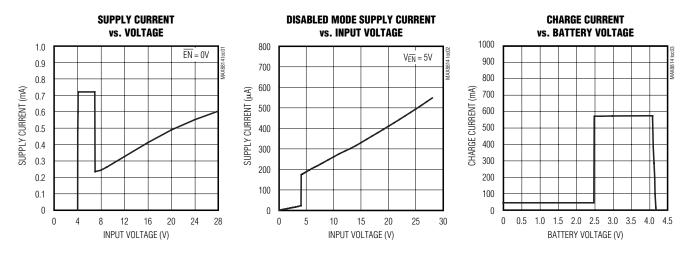
 $(V_{IN} = 5V, V_{BATT} = 4V, R_{\overline{POK}} = 1M\Omega$ to BATT, $\overline{EN} = 0V, R_{ISET} = 2.8k\Omega$ to GND, $C_{BATT} = 2.2\mu$ F, $T_A = -40^{\circ}$ C to +85°C, unless otherwise noted. Typical values are at $T_A = +25^{\circ}$ C.) (Note 1)

PARAMETER	C	ONDITIONS	MIN	ТҮР	MAX	UNITS
Regulator Dropout Voltage (VIN - VBATT)	$V_{BATT} = 4.1V$, $I_{BATT} = 4$	425mA		240	500	mV
EN Logic-Input Low Voltage	4.25V < V _{IN} < 6.5V				0.4	V
EN Logic-Input High Voltage	$4.25V < V_{IN} < 6.5V$		1.3			V
EN and ABI Internal Pulldown Resistance				200	400	kΩ
ABI Logic-Input Low Voltage	$V_{IN} = 0V$	$V_{IN} = 0V$			0.4	V
ABI Logic-Input High Voltage	$V_{IN} = 0V$	V _{IN} = 0V				V
ABO Output Low Voltage	I _{ABO (SINK)} = 1mA				0.4	V
ABO Output High Voltage	IABO (SOURCE) = 1mA		V _{BATT} - 0.4V			V
POK Output Low Voltage	I POK = 5mA				0.4	V
POK Output High Leakage		$T_A = +25^{\circ}C$		0.001	1	
Current	$V_{\overline{POK}} = 5.5V$	$T_A = +85^{\circ}C$		0.002		μΑ

Note 1: Specifications are 100% production tested at $T_A = +25^{\circ}$ C. Limits over the operating temperature range are guaranteed by design and characterization.

Typical Operating Characteristics

 $(V_{IN} = 5V, V_{BATT} = 4V, R_{\overline{POK}} = 1M\Omega$ to BATT, $\overline{EN} = 0V, R_{ISET} = 2.8k\Omega$ to GND, $C_{BATT} = 2.2\mu$ F, $T_A = -40^{\circ}$ C to +85°C, unless otherwise noted. Typical values are at $T_A = +25^{\circ}$ C.)



Typical Operating Characteristics (continued) WW atasheet4u.con **MAX881** $(V_{IN} = 5V, V_{BATT} = 4V, R_{POK} = 1M\Omega$ to BATT, $\overline{EN} = 0V, R_{ISET} = 2.8k\Omega$ to GND, $C_{BATT} = 2.2\mu$ F, $T_A = -40^{\circ}$ C to +85°C, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$.) **CHARGE CURRENT CHARGE CURRENT vs. INPUT** vs. INPUT VOLTAGE **VOLTAGE HEADROOM STARTUP INTO PRECHARGE** 1000 1000 $V_{BATT} = 2V$ 900 900 100mA/div 800 800 IBATT 0A CHARGE CURRENT (mA) CHARGE CURRENT (mA) 700 700 5V/div 600 V_{IN} 0V 600 500 500 400 400 VEN 5V/div 300 300 0V 200 200 $V_{BATT} = 4V$ V_{IN} RISING 100 100 5V/div 0 0 0V VPOK 0 4 8 12 16 20 24 28 0 100 200 300 400 500 40µs/div INPUT VOLTAGE (V) VIN - VBATT (mV) **BATTERY REGULATION VOLTAGE** SHUTDOWN (FAST-CHARGE TO SHUTDOWN) **ACCURACY vs. AMBIENT TEMPERATURE** CHARGE CURRENT vs. RISET 1.0 1000 BATTERY REGULATION VOLTAGE ACCURACY (%) $I_{BATT} = 0A$ 0.8 500mA/div IBATT 0.6 0A CHARGE CURRENT (mA) 0.4 100 5V/div VIN 0.2 0 0V -0.2 5V/div 10 -0.4 VEN 0V -0.6 5V/div -0.8 ٥V VPOK -1.0 1 100µs/div -40 -15 10 35 60 85 1 10 100 1000 10,000 TEMPERATURE (°C) $R_{ISFT}(k\Omega)$ **CHARGE CURRENT vs. AMBIENT** AUTOBOOT ENABLED AUTOBOOT ENABLED **TEMPERATURE BY ABI SIGNAL BY INPUT SUPPLY** MAX8814 toc1 IAX8814 to 700 $V_{BATT} = 4V$ 5V/div 600 5V/div CHARGE CURRENT (mA) 200 200 200 V_{ABI} 0V VAB 0V V_{BATT} = 3.2V 5V/div 5V/div V_{IN} VIN 0V 0V V_{BATT} 5V/div VBATT 5V/div 0V 0V 100 5V/div 5V/div $R_{ISET} = 2.8 k \Omega$ 0V 0V 0 VABO VABO -40 -15 10 35 60 85 4µs/div 4µs/div TEMPERATURE (°C)

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Pin Description

PIN	NAME	FUNCTION
1	IN	Input Supply Voltage. Bypass IN to GND with a $1\mu F$ or larger ceramic capacitor to minimize line noise and maximize input transient rejection.
2	GND	Ground. Connect GND and the exposed paddle to a large copper ground plane for maximum power dissipation. Connect GND to the exposed paddle directly under the IC.
3	ISET	Charge-Current Program and Fast-Charge Current Monitor. Output current from ISET is 877.2µA per ampere of battery charging current. Set the charging current by connecting a resistor (R2 in Figure 3) from ISET to GND. IFAST-CHARGE = 1596V / RISET. To configure the MAX8814 as a USB charger, see Figure 4.
4	ABI	Autobooting External Input. See the <i>Autobooting Assistant</i> section and Table 1 for autobooting conditions. ABI is pulled to GND through an internal 200k Ω resistor.
5	ABO	Autobooting Logic Output. See the <i>Autobooting Assistant</i> section and Table 1 for autobooting conditions.
6	ĒN	Logic-Level Enable Input. Drive $\overline{\text{EN}}$ high to disable charger. Drive $\overline{\text{EN}}$ low or leave unconnected for normal operation. $\overline{\text{EN}}$ has an internal 200k Ω pulldown resistor.
7	POK	Input-Voltage Status Indicator. Connect a 1M Ω pullup resistor from \overline{POK} to an external system supply. \overline{POK} is an open-drain output that asserts low when V _{IN} > 4.25V and (V _{IN} - V _{BATT}) ≥ 40mV. If V _{BATT} ≥ (V _{IN} - 40mV), the IC is shut down and \overline{POK} becomes high impedance.
8	BATT	Battery Connection. Bypass BATT to GND with a minimum of 2.2µF of capacitor.
	EP	Exposed Paddle. Connect the exposed paddle to a large ground plane for maximum power dissipation. Connect GND to the exposed paddle directly under the IC.

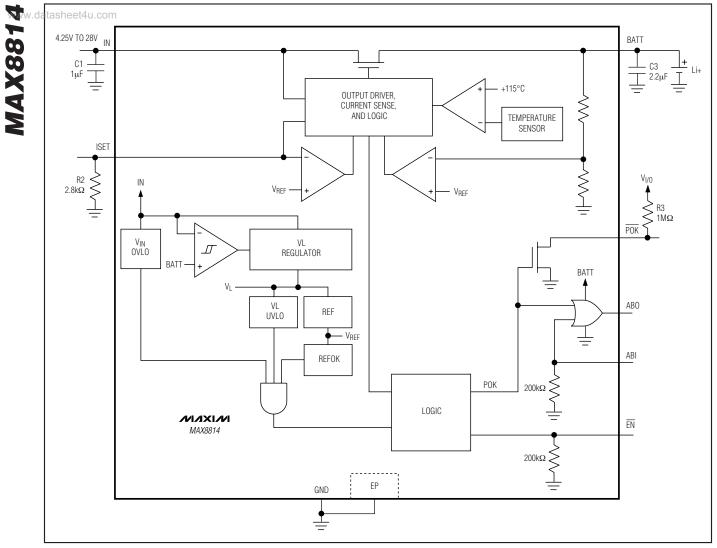


Figure 1. Functional Diagram

Detailed Description

The MAX8814 charger uses voltage, current, and thermal-control loops to charge a single Li+ cell and protect the battery (Figure 1). When a Li+ battery with a cell voltage below 2.5V is inserted, the MAX8814 charger enters the prequalification stage where it precharges that cell with 10% of the user-programmed fast-charge current (Figure 2). When the battery voltage exceeds 2.5V, the charger soft-starts as it enters the fast-charge stage. In the MAX8814, the fast-charge current level is programmed through a resistor from ISET to GND. As the battery voltage approaches 4.2V, the charging current is reduced. Once the battery voltage reaches 4.2V, the IC then enters a constant voltage regulation mode to maintain the battery at full charge.

Thermal Regulation

The thermal-regulation loop limits the MAX8814 die temperature to $+115^{\circ}$ C by reducing the charge current as necessary. This feature not only protects the IC from overheating, but also allows a higher charge current without risking damage to the IC.

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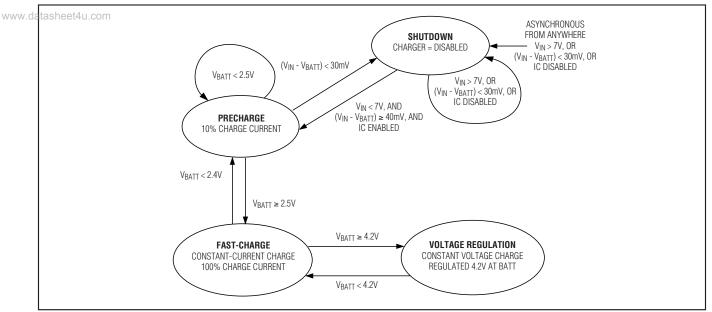


Figure 2. Charge-State Diagram

Charger Enable Input

The MAX8814 contains an active-low logic input (\overline{EN}) used to enable the charger. Drive \overline{EN} low, leave unconnected, or connect to GND to enable the chargercontrol circuitry. Drive \overline{EN} high to disable the chargercontrol circuitry. \overline{EN} has an internal 200k Ω pull-down resistor.

POK Output

The open-drain $\overrightarrow{\text{POK}}$ output asserts low when $V_{\text{IN}} \ge 4.25V$ and $(V_{\text{IN}} - V_{\text{BATT}}) \ge 40\text{mV}$ (typ, V_{IN} rising). $\overrightarrow{\text{POK}}$ requires an external pullup resistor (1M Ω typ) to an external power supply. $\overrightarrow{\text{POK}}$ is high impedance when $V_{\text{BATT}} \ge (V_{\text{IN}} - 40\text{mV})$.

Autobooting Assistant

The MAX8814 contains an autobooting assistant circuit that generates an enable signal for system booting (ABO). The booting assistant functions as an internal "OR" gate (Figure 1). The first input is dependent on the input voltage (VIN), and the second input is an external signal applied to ABI. The first input (POK) is driven high once VIN \geq 4.25V and (VIN - VBATT) \geq 40mV (typ, VIN rising).

The second input signal (ABI) is driven by an external source. ABI enables an autoboot signal (ABO high) when a battery is connected at BATT and is independent of \overrightarrow{POK} . If \overrightarrow{POK} is pulled low, the booting assistant always drives ABO high, regardless of ABI (see Table 1). ABI is pulled to GND through an internal 200k Ω resistor.

Table 1. ABO and POK States

ABI	BATT	POK	CHARGER STATE	ABO
Low	Present	Hi-Z	Shutdown	Low
High	Present	Hi-Z	Shutdown	High
х	Not present	Low	Fast-charge/voltage regulation	High
Х	Present	Low	Fast-charge/voltage regulation	High

X = Don't care.

If ABI is driven externally, a RC filter (R1 and C2 of Figure 3) is required for ESD protection and noise filtering. If ABI is supplied by a system's internal GPIO, or logic, the RC filter is not required.

Soft-Start

The soft-start algorithm activates when entering fastcharge mode. In the MAX8814, when the prequalification state is complete (V_{BATT} \geq 2.5V), the charging current ramps up in 250µs to the full charging current. This reduces the inrush current on the input supply.

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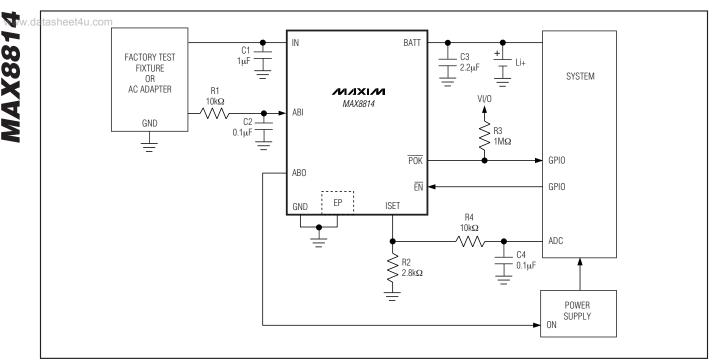


Figure 3. Microprocessor-Interfaced Li+ Battery Charger

Applications Information

Charge-Current Selection

The maximum charging current is programmed by an external resistor connected from ISET to GND (R_{ISET}). Calculate R_{ISET} as follows:

$$R_{ISET} = \frac{1596V}{I_{FAST-CHARGE}}$$

where IFAST-CHARGE is in amperes and RISET is in ohms. ISET can be used to monitor the fast-charge current level. The output current from ISET is 877.2µA per ampere of charging current. The output voltage at ISET is proportional to the charging current:

$$V_{\text{ISET}} = \frac{I_{\text{CHARGE}} \times R_{\text{ISET}}}{1140}$$

The voltage at ISET is nominally 1.4V at the selected fast-charge current and falls with charging current as the cell becomes fully charged or as the thermal-regulation circuitry activates.

Capacitor Selection

Connect a ceramic capacitor from BATT to GND for proper stability. Use a 2.2 μ F X5R ceramic capacitor for most applications. Connect a 1 μ F ceramic capacitor from IN to GND. Use a larger input bypass capacitor for high charging currents to reduce supply noise.

Thermal Considerations

The MAX8814 is available in a thermally enhanced TDFN package with an exposed paddle. Connect the exposed paddle to a large copper ground plane to provide a thermal contact between the device and the circuit board for increased power dissipation. The exposed paddle transfers heat away from the device, allowing the IC to charge the battery with maximum current, while minimizing the increase in die temperature.

DC Input Sources

The MAX8814 operates from a well-regulated DC source. The full charging input voltage range is 4.25V to 7V. The device can withstand up to 28V on the input without damage to the IC. If V_{IN} is greater than 7V, the internal overvoltage-protection circuitry disables charging until the input falls below 7V. An appropriate power supply must provide at least 4.25V at the desired peak charging current.



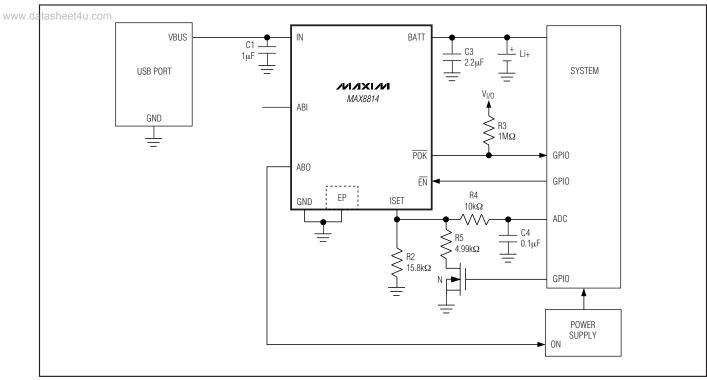


Figure 4. USB Battery Charger

Application Circuits

Microprocessor-Interfaced Charger

Figure 3 shows the MAX8814 as a microprocessorcooperated Li+ battery charger. The MAX8814 begins charging the battery when $\overline{\text{EN}}$ is low. The microprocessor can drive $\overline{\text{EN}}$ high to disable the charger. The MAX8814 generates a POK signal to indicate the presence of an input supply. By monitoring V_{ISET}, the system can measure the charging current and decide when to terminate the charge.

USB-Powered Li-Ion Charger

The universal serial bus (USB) provides a high-speed serial communication port as well as power for the remote device. The MAX8814 can be configured to charge a battery at the highest current possible from the host port. Figure 4 shows the MAX8814 as a USB battery charger. To make the circuit compatible with either 100mA or 500mA USB ports, the circuit initializes at 100mA charging current. The microprocessor then enumerates the host to determine its current capability. If the host port is capable, the charging current is increased to 425mA to avoid exceeding the 500mA USB specification.



Layout and Bypassing

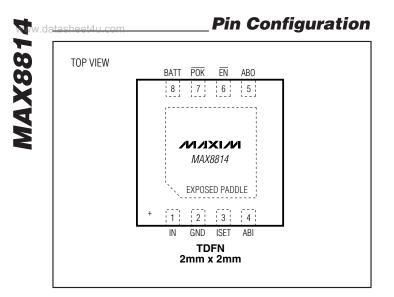
Place the input and output capacitors as close as possible to the IC. Provide a large copper ground plane to allow the exposed paddle to sink heat away from the IC. Connect the battery to BATT as close as possible to the IC to provide accurate battery voltage sensing. Make all high-current traces short and wide to minimize voltage drops. A sample layout is available in the MAX8814 Evaluation Kit to speed designs.

Chip Information

PROCESS: BICMOS

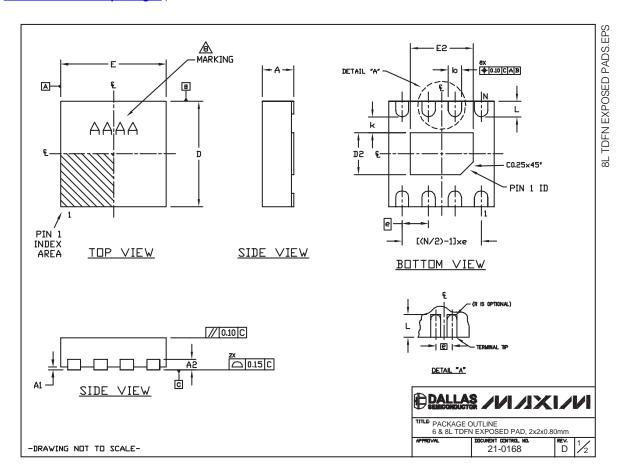
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MAX8814



Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to www.maxim-ic.com/packages.)



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Package Information (continued)

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to **www.maxim-ic.com/packages**.)

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COMMON DIMENSIONS					
SYMBOL	MIN.	MAX.			
A	0.70	0.80			
D	1.90	2.10			
E	1.90	2.10			
A1	0.00	0.05			
L	0.20	0.40			
k	0.25 MIN.				
A2	0.20 REF.				

PACKAGE VARI	PACKAGE VARIATIONS						
PKG. CODE	N	D2	E2	е	b	r	[(N/2)-1] x e
T622-1	6	0.90±0.10	1.60±0.10	0.65 TYP.	0.30±0.05	0.150	1.30 REF
T822-1	8	0.70±0.10	1.30±0.10	0.50 TYP.	0.25±0.05	0.125	1.50 REF
T822-2	8	0.80±0.10	1.20±0.10	0.50 TYP.	0.25±0.05	0.125	1.50 REF

NOTES:

- 1. ALL DIMENSIONS ARE IN mm. ANGLES IN DEGREES.
- 2. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS, COPLANARITY SHALL NOT EXCEED 0.08 mm.
- 3. WARPAGE SHALL NOT EXCEED 0.08 mm.
- 4. PACKAGE LENGTH/PACKAGE WIDTH ARE CONSIDERED AS SPECIAL CHARACTERISTIC(S).
- 5. COMPLY TO JEDEC MO229 EXCEPT D2 AND E2 DIMENSIONS.
- 6. "N" IS THE TOTAL NUMBER OF LEADS.
- 7. NUMBER OF LEADS SHOWN ARE FOR REFERENCE ONLY.
- $\underline{\mathscr{B}}$ marking is for package orientation reference only.

	PACKAGE OUTLINE 6 & 8L TDFN EXPOSED PAD, 2x2x0.80mm					
APTROVAL	21-0168	REV.	2/2			

-DRAWING NOT TO SCALE-

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